library IEEE;

use IEEE.STD\_LOGIC\_1164.ALL;

-- Uncomment the following library declaration if using

-- arithmetic functions with Signed or Unsigned values

--use IEEE.NUMERIC\_STD.ALL;

-- Uncomment the following library declaration if instantiating

-- any Xilinx primitives in this code.

--library UNISIM;

--use UNISIM.VComponents.all;

entity muxnew1 is

Port ( a : in STD\_LOGIC;

b : in STD\_LOGIC;

c : in STD\_LOGIC;

d : in STD\_LOGIC;

sel : in STD\_LOGIC\_VECTOR (1 downto 0);

y : out STD\_LOGIC);

end muxnew1;

architecture Behavioral of muxnew1 is

begin

y<=a when sel="00"else

b when sel="01"else

c when sel="10"else

d;

end Behavioral;

LIBRARY ieee;

USE ieee.std\_logic\_1164.ALL;

-- Uncomment the following library declaration if using

-- arithmetic functions with Signed or Unsigned values

--USE ieee.numeric\_std.ALL;

ENTITY muxnewtest IS

END muxnewtest;

ARCHITECTURE behavior OF muxnewtest IS

-- Component Declaration for the Unit Under Test (UUT)

COMPONENT muxnew1

PORT(

a : IN std\_logic;

b : IN std\_logic;

c : IN std\_logic;

d : IN std\_logic;

sel : IN std\_logic\_vector(1 downto 0);

y : OUT std\_logic

);

END COMPONENT;

--Inputs

signal a : std\_logic := '0';

signal b : std\_logic := '0';

signal c : std\_logic := '0';

signal d : std\_logic := '0';

signal sel : std\_logic\_vector(1 downto 0) := (others => '0');

--Outputs

signal y : std\_logic;

-- No clocks detected in port list. Replace <clock> below with

-- appropriate port name

-- constant <clock>\_period : time := 10 ns;

BEGIN

-- Instantiate the Unit Under Test (UUT)

uut: muxnew1 PORT MAP (

a => a,

b => b,

c => c,

d => d,

sel => sel,

y => y

);

-- Clock process definitions

-- <clock>\_process :process

--begin

-- <clock> <= '0';

--wait for <clock>\_period/2;

-- <clock> <= '1';

-- wait for <clock>\_period/2;

-- end process;

-- Stimulus process

stim\_proc: process

begin

-- hold reset state for 100 ns.

a<='1';

b<='0';

c<='1';

d<='0';

sel<="00";

wait for 100 ns;

sel<="01";

wait for 100 ns;

sel<="10";

wait for 100 ns;

sel<="11";

wait for 100 ns;

-- wait for <clock>\_period\*10;

-- insert stimulus here

wait;

end process;

END;

